ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention Methodology for Verifying Multi-Cycle and Clock-Domain-Crossing Logic Using Random Flip-Flop Delays

Submision Type: Utility Patent Filing

Application Number:

10/604879

EFS ID: 46373

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First Named Applicant: Hin-Kwai Lee

Attorney Docket Number: NM-103

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File Listing:

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			2003-08-23	
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